

## TRANSMITTAL

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<b>Title of Invention</b>	METHOD AND APPARATUS FOR PERFORMING POWER ROUTING ON A VOLTAGE ISLAND WITHIN AN INTEGRATED CIRCUIT CHIP	
<p>Application Number : Date : First Named Applicant: Bing Chen Confirmation Number: Attorney Docket Number: BUR920020141US1</p>		
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Submitted By:	Elec. Sign.	Sign. Capacity
Richard M. Kotulak Esq. Registered Number: 27.712	Richard M. Kotulak /s/	Attorney

Documents being submitted:	Files
us-assignment	BUR920020141US1efs-usassn.xml us-assignment.xsl us-assignment.dtd BUR920020141US1Assignment1.tif BUR920020141US1Assignment2.tif BUR920020141US1Assignment3.tif BUR920020141US1Assignment4.tif BUR920020141US1Assignment5.tif BUR920020141US1efs-usrequ.xml us-request.dtd us-request.xsl us-fee-sheet.dtd BUR920020141US1efs-usfees.xml us-fee-sheet.xsl us-declaration
us-request	BUR920020141US1Dec1.tif BUR920020141US1Dec2.tif BUR920020141US1Dec3.tif BUR920020141US1Dec4.tif BUR920020141US1Dec5.tif BUR920020141US1Spec-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfr.ent isomopf.ent isomsr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isounum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd BUR920020141US1FIGSsheet1of5.tif BUR920020141US1FIGSsheet2of5.tif
us-fee-sheet	
us-declaration	
us-declaration	
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application-body	

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**Comments**

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